

REMARKS

Claims 5, 8, 10, 11, 33, 37, 47, 48, 53, 54, 70-72, 74 and 75 are pending in this application, with claims 5, 8 and 70 being independent. Claims 5, 8 and 70 have been amended and claims 76-81 have been canceled.

The drawings have been amended to add the legend "Prior Art" and to overcome the drawing objections.

Claims 5, 8, 10, 11, 33, 37, 47, 48, 53, 54, 70-72, 74 and 75 have been rejected as being unpatentable over McKnight (U.S. Patent No. 5,959,598).

With respect to claim 5 and its dependent claims, applicant requests reconsideration and withdrawal of this rejection because McKnight does not describe or suggest a display device in which each pixel has  $n \times m$  memory circuits and  $n$  gate signal lines, where  $n$  is a natural number equal to or greater than 2 and  $m$  is a natural number. While the action indicates that the storage capacitor 805 of Fig. 8 corresponds to the recited  $n \times m$  memory circuits, this cannot be the case because the storage capacitor is only a single device such that, in the device of McKnight, " $n$ " would be 1 and would not be equal to or greater than 2. Moreover, while Fig. 11 of McKnight describes a pixel including a  $n$ -bit data latch 1005, input to the latch 1005 is controlled using a single gate wire 779 (i.e., a single gate signal line). As such, Fig. 11 of McKnight also does not describe or suggest the  $n \times m$  memory circuits and  $n$  gate signal lines recited in claim 5. Accordingly, applicant requests reconsideration and withdrawal of the rejection of claim 5 and its dependent claims.

Independent claim 8 similarly recites pixels that each include  $n$  ( $n$  is a natural number equal to or greater than 2) gate signal lines and  $n \times m$  ( $m$  is a natural number) memory circuits. Accordingly, applicant requests reconsideration and withdrawal of the rejection of claim 8 and its dependent claims for the reasons discussed above with respect to claim 5.

Likewise, independent claim 70 recites pixels that each include  $n \times m$  ( $n$  is a natural number equal to or greater than 2,  $m$  is a natural number) memory circuits and  $n$  gate signal lines. Accordingly, applicant requests reconsideration and withdrawal of the rejection of claim 70 and its dependent claims for the reasons discussed above with respect to claim 5.

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Claims 10 and 11 have been rejected as being unpatentable over McKnight in view of Sung (U.S. Patent No. 6,535,192). Applicant requests reconsideration and withdrawal of this rejection because Sung does not remedy the failure of McKnight to describe or suggest the subject matter of claim 8 from which claims 10 and 11 depend.

No fees are believed due. Please apply any other charges or credits to deposit account 06-1050.

Respectfully submitted,

  
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